

	L #	Hits	Search T xt	DB
1	L1	69	(361/734,738).cccls.	USP AT; US-P GPU B
2	L2	59	1 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
3	L3	23	2 and ((power voltage) with (plane\$1 layer\$1))	USP AT; US-P GPU B
4	L4	36	2 not 3	USP AT; US-P GPU B
5	L5	4	("4803595" "5177594" "5629838" "5977640").PN.	USP AT
6	L6	120	361/306.2.cccls.	USP AT; US-P GPU B
7	L7	45	6 and ((power voltage reference) with (plane\$1 layer\$1))	USP AT; US-P GPU B
8	L8	67	(6 not 7) and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B

	L #	Hit	S arch T xt	DB
9	L9	366	(361/780,793,794).cccls.	USP AT; US-P GPU B
10	L10	186	9 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
11	L11	157	10 and ((power voltage reference) with (plane\$1 layer\$1))	USP AT; US-P GPU B
12	L12	240	361/782.cccls.	USP AT; US-P GPU B
13	L13	158	12 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
14	L14	55	13 and ((power voltage reference) with (plane\$1 layer\$1))	USP AT; US-P GPU B
15	L15	194	11 14	USP AT; US-P GPU B

	L #	Hits	S arch T xt	DBs
16	L18	45	malladi\$.in.	USP AT; US-P GPU B
17	L19	8	18 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
18	L20	842	257/777.ccls.	USP AT; US-P GPU B
19	L21	195	20 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
20	L22	72	21 and ((power voltage reference) with (plane\$1 layer\$1))	USP AT; US-P GPU B
21	L23	2204	(257/691,700,724).ccls.	USP AT; US-P GPU B
22	L24	665	23 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B

	L #	Hits	S arch Text	DBs
23	L25	352	24 and ((power voltage reference) with (plane\$1 layer\$1))	USP AT; US-P GPU B
24	L26	289	25 not (2 7 8 15 22)	USP AT; US-P GPU B